



Department of Electronics and Telecommunication Engineering

Shri Guru Gobind Singhji Institute of Engineering and Technology, Nanded

(An Autonomous Institute of Govt. of Maharashtra)

in association with

Intel Technologies Ltd., Bengaluru

organises

3-days Online Faculty Development Programme on

“Embedded System Design using Intel SoC FPGAs”

January 27-29, 2022

Outline

Embedded System Design has become one of the sought after area in today's era of Industry 4.0. Our goal is to engage with *educators* and create awareness of and interest in concepts, tools and skills that are key to develop skilled manpower in this area. This will serve as a foundation for the academia to build upon and make SoCs and FPGAs an integral part of their teaching process. This FDP is focused on exposing participants to latest tools related to digital system design for applications using FPGA and SoC FPGAs. The participants will gain a better understanding of utilizing FPGA based control techniques to solve engineering challenges. Participants will be able to apply the experience gained from FDP to strengthen the core engineering skills using the hardware FPGA kits & Intel Quartus Prime design software. *We look forward to exciting and interactive sessions.*

Target Participants

The faculty members of the AICTE approved institutions, Research scholars, and PG Scholars. *Total number of participants is limited to 1000 on the first-come-first-served basis.* All the participants who attend all the sessions and successfully complete the MCQ test at the end of FDP with 60% or more marks will get the e-certificate (pdf).

Mode of delivery

The FDP will be delivered **online** through Cisco Webex platform. Participants need to provide email id registered on Webex. The responsibility of registering at Webex solely lies with the participants. The participants can join through Desktop/Laptop or Android/iOS platforms on cellphone.

Expert Speakers



Israr Sheikh

Intel PSG Field Application Engineering Manager for India, ASEAN and ANZ Region, supporting FPGA Products

Israr lead the customer support, enablement and co-architect Innovation products that target emerging FPGA usage models across the Embedded, Telecom, Network, Compute and Custom Designs. FPGA based Hardware Accelerators, Heterogeneous Co-processing, Edge and AI are the recent customer enablement programs which he is managing. He also participates in Intel India Site Technology office Innovation programs, startup-eco system enablement, Academia & Research for FPGA and System Design. Israr is frequent speaker and Intel and industry events. Israr has over 20 years of experience in FPGA based system design, RTL and validations. He holds bachelor's degree in Electronics & Communications Engineering from the Amaravati University and PGDBA from Symbiosis Pune.



Padmanaban K.

Software Enabling and Optimization Engineer in Customer Experience Group at Intel PSG

Padmanaban is the Software Enabling and Optimization Engineer in Customer Experience Group at Intel PSG. Prior to joining Intel, he worked as a Chief Faculty in Sandeepani School of VLSI design (Training division of CoreEL Technologies, Bangalore) and as an Assistant Professor and Project Coordinator in MSRSAS, Bangalore. Padmanaban has a post graduate degree in Applied Electronics from Anna University, Chennai. He has 14+ years of experience in digital design for both FPGA and ASIC.

Register for the FDP at <https://cutt.ly/esfdpwithintel>

3-days Online Faculty Development Programme on “Embedded System Design using Intel SoC FPGAs” January 27-29, 2022

Organised by **Shri Guru Gobind Singhji Institute of Engineering and Technology, Nanded** in association with **Intel Technologies Ltd., Bengaluru**

Topics and Hands-on sessions (The proposed schedule might change a little bit, but the theme of the FDP will be followed)

Day	Date	9:30 AM to 11:30 AM	11:45 AM to 1:30 PM
1	27-Jan-2022	Introduction to Intel FPGAs and Quartus Tool Flow	FPGA design and implementation– Virtual labs with remote console
2	28-Jan-2022	Introduction to Intel SoC FPGAs– IP design and Platform designer	SoC Design Flow–Basic SoC lab demo
3	29-Jan-2022	Embedded System design using Cyclone V and ARM	SoC EDS design flow and hands-on lab

Link to download software required for the sessions will be sent to the participants 2 days in advanced so that they can install the same on their machines.

Registration Details and Terms for Certificate

There is no registration fee. Faculty members of the AICTE approved institutions, Research Scholars, and PG students can join. Total number of participants is limited to **1000**. Certificates shall be issued to those who register, **attend the program with 100% attendance, and score minimum 60% marks in the MCQ test after FDP**. Scan the QR code given here to go to the Google Form. You can also visit the URL <https://cutt.ly/esdfdpwithintel> to register.



Coordinators

Dr. Suhas S. Gajre

Professor and Dean (Incubation)
SGGSIET, Nanded

Mr. Padmanaban K.

Program Specialist
India FPGA University Outreach
Intel Programmable Solutions Group

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Director, SGGSIET, Nanded

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Social Media

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Shri Guru Gobind Singhji Institute of Engineering and Technology, Nanded is AICTE approved, NBA accredited, Government-aided autonomous Institute, affiliated to Swami Ramanand Teerth Marathwada University, Nanded. Established in 1981, it is one of the promising autonomous institutions in technical education, research and technology transfer. It offers 10 UG and 10 PG courses and also offer Ph.D. programme. Institute has more than 50% of regular faculty with Ph.D. Faculty have contributed for laboratory development through proposals to various funding agencies like AICTE, DST-FIST, BARC, NRB, etc. We also got funding of TEQIP in all 3 phases. Establishment of Innovation Laboratory, Institution's Innovation Council, Incubation Centre, and Engineering Exploration laboratory has led to culture of innovation. Students get exposure to multidisciplinary and innovative projects. SGGSIET has secured 4th Rank in ARIIA 2020 in Govt. and Govt. aided Category. It is also one of the 49 institutes in India, and the only institute in Marathwada and Vidarbha region to get selected for the prestigious AICTE-IDEA Laboratory.

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Intel Corporation is an American multinational corporation and technology company headquartered in Santa Clara, California, USA. It is the world's largest semiconductor chip manufacturer by revenue and is the developer of the processors found in most personal computers. The Indian HQ is based in Bengaluru & caters to all the needs of the chip designs, software and hardware solutions for all types of modern digital computing applications in all fields, i.e., from industry to academia to research.

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